The listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

- 1.-24. (Canceled)
- 25. (Currently Amended) A transmission device <u>for 4-value FSK modulation</u> for transmitting data using four symbol values, the transmission device comprising:

a division unit configured to be inputted with original data arranged in order from one with highest importance, the original data comprising first data and second data after the first data, the first data being [to be] protected and the second data being [to be] unprotected, and to divide the first data into first single bit data components by one bit and divides divide the second data into second two bit data by two bits;

a redundant bit addition unit configured to add a redundant bit as a lower order bit to each of the first <u>single</u> bit data <u>components</u> as a higher-order-bit to create <u>first two</u> <u>bit</u> 2-bits data which results in a symbol having any one of two symbol values whose gain is higher at the Nyquist point, of the four symbol values, <u>wherein the redundant bit</u> is <u>determined</u> so that the created first two bit data corresponds to any one of specific two symbol values of the four symbol values, an interval between the specific two symbol values being largest among the four symbol values; and

a modulation unit configured to perform $\underline{\text{for 4-value FSK}}$ modulation on the basis of the first $\underline{\text{two}}$ bit data with the redundant bit being added and the second $\underline{\text{two}}$ bit data.

26. (Currently Amended) The transmission device according to claim 25, wherein each of the first data [to be] being protected comprises flag data.

- 27. (Currently Amended) The transmission device according to claim 25, wherein the original data includes bits for error check and the first data [to be] being protected includes [Ithe]] bits for error check.
- 28. (Currently Amended) The transmission device according to claim 25, wherein the original data includes bits for error correction and the first data [to be] being protected includes [fthe]] bits for error correction.
- 29. (Currently Amended) The transmission device according to claim 25, wherein the number of the first data [to be] <u>being</u> protected is less than the number of the second data [to be] being unprotected.
- 30. (Currently Amended) The transmission device according to claim 25, wherein the original data represents a plurality of pieces of information, and the redundant bit addition unit operates for respective ones of the plurality of pieces of information to add the redundant bit to each of the first data [to be] being protected to generate coded data.

31. (Currently Amended) A reception device comprising:

- a_demodulation_unit_configured_to_receive_and_demodulate <u>for receiving</u> a transmission signal <u>of 4-value FSK modulation</u> in which data has been transmitted using four symbol values, the <u>reception device comprising:</u>
- a demodulation unit configured to receive and demodulate the transmission signal, wherein the transmission signal is obtained by inputting original data arranged in order from one with highest importance, the original data comprising first data and second data after the first data, the first data being [to be] protected and the second data being [to be] unprotected, dividing the first data into first single bit data components by one bit and dividing the second data into second two bit data, data by two bits adding

a redundant bit as a lower order bit to each of the first single bit data components as a higher order bit to create first two bit 2-bits data which results in a symbol having any one of two symbol values whose gain is higher at the Nyquist point, of the four symbol values. wherein the redundant bit is determined so that the created first two bit data corresponds to any one of specific two symbol values of the four symbol values, an interval between the specific two symbol values being largest among the four symbol values, and performing 4-value FSK modulation on the basis of the first two bit data with the redundant bit being added and the second two bit data;

- a symbol decision unit configured to perform a symbol decision at each Nyquist interval for the signal demodulated by the demodulation unit:
- a bit conversion unit configured to convert a symbol value obtained by the symbol decision performed by the symbol decision unit into a bit value; and
- a data recovery unit configured to compose a data string by deleting the added redundant bit from the data of the bit value converted by the bit conversion unit, to restore original data.
- 32. (Previously Presented) The reception device according to claim 31, wherein the demodulation unit demodulates the received signal by converting the received signal into a signal of a voltage corresponding to a frequency of the received signal, and the symbol decision unit performs the symbol decision by comparing the voltage of the signal, which has been demodulated by the demodulation unit, with preset threshold values.
- 33. (Currently Amended) A method for transmitting data using four symbol values for 4-value FSK modulation, the method comprising the steps of:

inputting original data arranged in order from one with highest importance, the original data comprising first data and second data after the first data, the first data being [to be] protected and the second data being [to be] unprotected, dividing the first data into first single bit data components by one-bit and dividing the second data into second two bit data by two bits;

adding a redundant bit as a lower order bit to each of the first single bit data components as a higher order bit to create first two bit 2 bits data which results in a symbol having any one of two symbol values whose gain is higher at the Nyquist point, of the four symbol values, wherein the redundant bit is determined so that the created first two bit data corresponds to any one of specific two symbol values of the four symbol values, an interval between the specific two symbol values being largest among the four symbol values, and

performing 4-value FSK modulation on the basis of the first two bit data with the redundant bit being added and the second two bit data.

34. (Currently Amended) A method for receiving data comprising the steps of: receiving and demodulating a transmission signal in which data has been transmitted using four symbol [[value]] values, wherein the transmission signal is obtained by inputting original data arranged in order from one with highest importance, the original data comprising first data and second data after the first data, the first data being Ito be protected and the second data being Ito be unprotected, dividing the first data into first single bit data components by one bit and dividing the second data into second two bit data, data by two-bits adding a redundant bit as a lower order bit to each of the first single bit data components as a higher order bit to create first two bit 2 bits data which results in a symbol having any one of two symbol values whose gain is higher at the Nyquist point, of the four symbol values, wherein the redundant bit is determined so that the created first two bit data corresponds to any one of specific two symbol values of the four symbol values, an interval between the specific two symbol values being largest among the four symbol values, and performing 4-value FSK modulation on the basis of the first two bit data with the redundant bit being added and the second two bit data;

performing a symbol decision at each Nyquist interval for the signal demodulated in the demodulating step;

converting a symbol value obtained by the symbol decision performed in the symbol deciding step into a bit value; and

composing a data string by deleting the added redundant bit from the data of the bit value converted in the symbol value converting step, to restore original data.